



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 4280

SERIAL NUMBER 10/761,604	FILING DATE 01/20/2004  RULE	CLASS 713	GROUP ART UNIT 2116	ATTORNEY DOCKET NO. 42P8821C
-----------------------------	---------------------------------------	--------------	------------------------	------------------------------------

## APPLICANTS

Jonathan H. Liu, Folsom, CA;

Hing Y. To, Folsom, CA;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/608,343 06/29/2000 PAT 6,725,390

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/23/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 7	TOTAL CLAIMS 16	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials		

## ADDRESS

08791  
 BLAKELY SOKOLOFF TAYLOR & ZAFMAN  
 12400 WILSHIRE BOULEVARD  
 SEVENTH FLOOR  
 LOS ANGELES , CA  
 90025-1030

## TITLE

Optimization of integrated circuit device I/O bus timing

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
RECEIVED		

770		<input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit _____
-----	--	---